

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	65	reverse adj bias adj resistance	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:04
L2	1997	365/158.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:04
L3	2012	365/171.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:04
L4	1172	365/173.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:04
L5	54	eaton\$-james-r\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:05
L6	346	perner-frederick-a\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:05
L7	188	tran-lung-t\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:05
L8	83	eldredge-kenneth-j\$.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2007/11/09 09:06

Day : Friday
Date: 11/9/2007

Time: 09:00:45


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = EATON

First Name = JAMES

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11215531	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215627	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11215758	Not Issued	161	08/29/2005	Identification of laterally positioned servo bands employing differentiating characteristics of servo patterns	EATON, JAMES HOWARD
11830869	Not Issued	30	07/31/2007	TRI-STATE SERVOWRITER DRIVER WITH SLOW RETURN TO ZERO	EATON, JAMES HOWARD
08859830	5930065	150	05/16/1997	TIMING BASED SERVO LONGITUDINAL ADDRESSING	EATON, JAMES HOWARD
08865540	6021013	150	05/29/1997	TIMING BASED SERVO SYSTEM FOR MAGNETIC TAPE SYSTEMS	EATON, JAMES HOWARD
08896356	5883770	250	07/18/1997	PARTIAL WIDTH MASS PRODUCED LINEAR TAPE RECORDING HEAD	EATON, JAMES HOWARD
08896782	5905613	250	07/18/1997	BIDIRECTIONAL FLAT CONTOUR LINEAR TAPE RECORDING HEAD AND DRIVE	EATON, JAMES HOWARD
09067490	6043963	150	04/27/1998	LOW-PROFILE TAPE CARTRIDGE	EATON, JAMES HOWARD
09188997	6341416	150	11/09/1998	METHOD FOR MASS PRODUCING PARTIAL WIDTH LINEAR TAPE	EATON, JAMES HOWARD

				RECORDING HEAD	
<u>09420889</u>	<u>6282051</u>	150	10/19/1999	TIMING BASED SERVO SYSTEM FOR MAGNETIC TAPE SYSTEMS	EATON, JAMES HOWARD
<u>11498597</u>	Not Issued	30	08/02/2006	Information system and method for generating appraisal reports for real properties	EATON, JAMES M.
<u>08198714</u>	Not Issued	166	02/18/1994	POWER CONTROL SYSTEM FOR A THERMAL INK-JET PRINTER	EATON, JAMES N.
<u>08509396</u>	<u>5563635</u>	150	07/31/1995	POWER CONTROL SYSTEM FOR A THERMAL INK-JET PRINTER	EATON, JAMES N.
<u>07741000</u>	Not Issued	161	08/06/1991	RADIO-CONTROLLED MODEL SUSPENSION SYSTEM	EATON, JAMES O.
<u>07997858</u>	<u>5312044</u>	150	12/29/1992	PRESSURE CLEANING METHOD AND APPARATUS	EATON, JAMES O.
<u>08368488</u>	<u>5833514</u>	250	01/04/1995	REACTIONARY FORCE UTILIZATION	EATON, JAMES O.
<u>09934548</u>	<u>6864529</u>	150	08/23/2001	THIN FILM TRANSISTOR MEMORY DEVICE	EATON, JAMES R.
<u>09976304</u>	<u>6501697</u>	150	10/11/2001	HIGH DENSITY MEMORY SENSE AMPLIFIER	EATON, JAMES R.
<u>10006723</u>	<u>6661704</u>	150	12/10/2001	DIODE DECOUPLED SENSING METHOD AND APPARATUS	EATON, JAMES R.
<u>10138076</u>	<u>6760245</u>	150	05/01/2002	MOLECULAR WIRE CROSSBAR FLASH MEMORY	EATON, JAMES R.
<u>10145337</u>	<u>6738307</u>	150	05/13/2002	ADDRESS STRUCTURE AND METHODS FOR MULTIPLE ARRAYS OF DATA STORAGE MEMORY	EATON, JAMES R.
<u>10151913</u>	Not Issued	161	05/22/2002	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	EATON, JAMES R.
<u>10346494</u>	<u>6839270</u>	150	01/17/2003	SYSTEM FOR AND METHOD OF ACCESSING A FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY	EATON, JAMES R.
<u>10346700</u>	<u>6842389</u>	150	01/17/2003	SYSTEM FOR AND METHOD OF FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY CELL AND DECODING SCHEME	EATON, JAMES R.

<u>10661448</u>	Not Issued	71	09/11/2003	Increased magnetic memory array sizes and operating margins	EATON, JAMES R.
<u>10676465</u>	<u>6927996</u>	150	09/30/2003	MAGNETIC MEMORY DEVICE	EATON, JAMES R.
<u>10697776</u>	Not Issued	161	10/30/2003	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	EATON, JAMES R.
<u>10777000</u>	<u>6781918</u>	150	02/10/2004	ADDRESS STRUCTURE AND METHODS FOR MULTIPLE ARRAYS OF DATA STORAGE MEMORY	EATON, JAMES R.
<u>10985762</u>	<u>7161838</u>	150	11/09/2004	THIN FILM TRANSISTOR MEMORY DEVICE	EATON, JAMES R.
<u>08199400</u>	<u>5445043</u>	250	02/22/1994	HYDRAULIC SHIFT CONTROL FOR A POWER TRANSMISSION	EATON, JAMES R.
<u>08304892</u>	<u>5509868</u>	150	09/13/1994	GOVERNOR INTERLOCK VALVE	EATON, JAMES R.
<u>08310448</u>	<u>5496231</u>	150	09/22/1994	HYDRAULIC CONTROL FOR A MULTI-SPEED POWER TRANSMISSION	EATON, JAMES R.
<u>08310456</u>	<u>5492027</u>	150	09/22/1994	ROTARY SHIFT CONTROL VALVING MECHANISM FOR A POWER TRANSMISSION	EATON, JAMES R.
<u>10347723</u>	<u>6826112</u>	150	01/21/2003	LOW POWER LOGIC GATE	EATON, JAMES ROBERT
<u>10348645</u>	<u>6940770</u>	150	01/21/2003	METHOD FOR PRECHARGING WORD AND BIT LINES FOR SELECTING MEMORY CELLS WITHIN A MEMORY ARRAY	EATON, JAMES ROBERT
<u>11129269</u>	<u>7248518</u>	150	05/12/2005	SELF-TIMED MEMORY DEVICE PROVIDING ADEQUATE CHARGING TIME FOR SELECTED HEAVIEST LOADING ROW	EATON, JAMES ROBERT
<u>08310447</u>	<u>5492210</u>	150	09/22/1994	RATIO INTERCHANGE CONTROL VALVING	EATON, JAMES RUSSELL
<u>06373861</u>	<u>4468903</u>	150	05/03/1982	BUILDING PANEL	EATON, JAMES W.
<u>06374190</u>	<u>4468909</u>	150	05/03/1982	BUILDING PANEL	EATON, JAMES W.
<u>06374281</u>	<u>4422266</u>	150	05/03/1982	BUILDING PANEL	EATON, JAMES W.
<u>06427543</u>	<u>4464872</u>	150	09/29/1982	BUILDING PANEL	EATON, JAMES W.

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	<input type="button" value="Search"/>
	<input type="text" value="eaton"/>	<input type="text" value="james"/>	

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Day : Friday
Date: 11/9/2007

Time: 09:01:51

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = PERNER

First Name = FREDERICK

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10223490	6728153	150	08/19/2002	MAGNETIC MEMORY WHICH COMPARES FIRST MEMORY CELL DATA BITS AND SECOND DATA BITS	PERNER, FREDERICK A.
10261532	6754123	150	10/01/2002	ADJUSTABLE CURRENT MODE DIFFERENTIAL AMPLIFIER FOR MULTIPLE BIAS POINT SENSING OF MRAM HAVING DIODE ISOLATION	PERNER, FREDERICK A.
10262051	6674679	150	10/01/2002	ADJUSTABLE CURRENT MODE DIFFERENTIAL AMPLIFIER FOR MULTIPLE BIAS POINT SENSING OF MRAM HAVING EQUI-POTENTIAL ISOLATION	PERNER, FREDERICK A.
10264588	6834017	150	10/03/2002	ERROR DETECTION SYSTEM FOR AN INFORMATION STORAGE DEVICE	PERNER, FREDERICK A.
10273623	6678197	150	10/18/2002	SYSTEMS AND METHODS FOR REDUCING THE EFFECT OF NOISE WHILE READING DATA FROM MEMORY	PERNER, FREDERICK A.
10289997	Not Issued	161	11/06/2002	Imaging system with non-volatile memory	PERNER, FREDERICK A.
10293027	6891768	150	11/13/2002	POWER-SAVING READING OF MAGNETIC MEMORY DEVICES	PERNER, FREDERICK A.
10346494	6839270	150	01/17/2003	SYSTEM FOR AND METHOD OF ACCESSING A FOUR-CONDUCTOR MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
10346700	6842389	150	01/17/2003	SYSTEM FOR AND METHOD OF FOUR-CONDUCTOR	PERNER, FREDERICK A.

				MAGNETIC RANDOM ACCESS MEMORY CELL AND DECODING SCHEME	
<u>10350398</u>	<u>6807118</u>	150	01/23/2003	ADJUSTABLE OFFSET DIFFERENTIAL AMPLIFIER	PERNER, FREDERICK A.
<u>10425362</u>	<u>6930914</u>	150	04/29/2003	METHODS FOR ISOLATING MEMORY ELEMENTS WITHIN AN ARRAY	PERNER, FREDERICK A.
<u>10426381</u>	<u>6667901</u>	150	04/29/2003	DUAL-JUNCTION MAGNETIC MEMORY DEVICE AND READ METHOD	PERNER, FREDERICK A.
<u>10444477</u>	<u>6956762</u>	150	05/23/2003	METHOD AND SYSTEM FOR DATA COMMUNICATION ON A CHIP	PERNER, FREDERICK A.
<u>10448574</u>	<u>6873543</u>	150	05/30/2003	MEMORY DEVICE	PERNER, FREDERICK A.
<u>10449572</u>	<u>7027318</u>	150	05/30/2003	METHOD AND SYSTEM FOR ADJUSTING OFFSET VOLTAGE	PERNER, FREDERICK A.
<u>10459635</u>	<u>6865104</u>	150	06/10/2003	MAGNETORESISTIVE RANDOM ACCESS MEMORY (MRAM) WITH ON-CHIP AUTOMATIC DETERMINATION OF OPTIMIZED WRITE CURRENT METHOD AND APPARATUS	PERNER, FREDERICK A.
<u>10601895</u>	<u>6865107</u>	150	06/23/2003	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
<u>10609278</u>	<u>6954373</u>	150	06/27/2003	APPARATUS AND METHOD FOR DETERMINING THE LOGIC STATE OF A MAGNETIC TUNNEL JUNCTION MEMORY DEVICE	PERNER, FREDERICK A.
<u>10614504</u>	<u>6836422</u>	150	07/07/2003	SYSTEM AND METHOD FOR READING A MEMORY CELL	PERNER, FREDERICK A.
<u>10614505</u>	<u>6842364</u>	150	07/07/2003	MEMORY CELL STRINGS IN A RESISTIVE CROSS POINT MEMORY CELL ARRAY	PERNER, FREDERICK A.
<u>10614581</u>	<u>6865108</u>	150	07/07/2003	MEMORY CELL STRINGS IN A RESISTIVE CROSS POINT MEMORY CELL ARRAY	PERNER, FREDERICK A.
<u>10623357</u>	<u>6898134</u>	150	07/18/2003	SYSTEMS AND METHODS FOR SENSING A MEMORY ELEMENT	PERNER, FREDERICK A.

<u>10625016</u>	<u>7079148</u>	150	07/23/2003	NON-VOLATILE MEMORY PARALLEL PROCESSOR	PERNER, FREDERICK A.
<u>10635399</u>	<u>6751147</u>	150	08/05/2003	METHOD FOR ADAPTIVELY WRITING A MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
<u>10635405</u>	<u>6826086</u>	150	08/05/2003	METHOD, APPARATUS AND SYSTEM FOR ERASING AND WRITING A MAGNETIC RANDOM ACCESS MEMORY	PERNER, FREDERICK A.
<u>10640792</u>	<u>6961262</u>	150	08/13/2003	MEMORY CELL ISOLATION	PERNER, FREDERICK A.
<u>10649076</u>	<u>6947313</u>	150	08/27/2003	METHOD AND APPARATUS OF COUPLING CONDUCTORS IN MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10649078</u>	<u>7221582</u>	150	08/27/2003	METHOD AND SYSTEM FOR CONTROLLING WRITE CURRENT IN MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10649752</u>	<u>6901005</u>	150	08/27/2003	METHOD AND SYSTEM READING MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10650278</u>	<u>7006388</u>	150	08/28/2003	MEMORY WITH REFERENCE- INITIATED SEQUENTIAL SENSING	PERNER, FREDERICK A.
<u>10656758</u>	<u>6927092</u>	150	09/05/2003	A METHOD OF FORMING A SHARED GLOBAL WORD LINE MRAM STRUCTURE	PERNER, FREDERICK A.
<u>10657385</u>	<u>6937509</u>	150	09/08/2003	DATA STORAGE DEVICE AND METHOD OF FORMING THE SAME	PERNER, FREDERICK A.
<u>10658442</u>	<u>6791873</u>	150	09/08/2003	APPARATUS AND METHOD FOR GENERATING A WRITE CURRENT FOR A MAGNETIC MEMORY CELL	PERNER, FREDERICK A.
<u>10660831</u>	<u>6999334</u>	150	09/12/2003	SYSTEM AND METHOD FOR DETERMINING THE LOGIC STATE OF A MEMORY CELL IN A MAGNETIC TUNNEL JUNCTION MEMORY DEVICE	PERNER, FREDERICK A.
<u>10661448</u>	Not Issued	71	09/11/2003	Increased magnetic memory array sizes and operating margins	PERNER, FREDERICK A.
<u>10675191</u>	<u>6842390</u>	150	09/30/2003	SYSTEMS AND METHODS FOR COMMUNICATING WITH MEMORY BLOCKS	PERNER, FREDERICK A.

<u>10675740</u>	<u>7079436</u>	150	09/30/2003	RESISTIVE CROSS POINT MEMORY	PERNER, FREDERICK A.
<u>10678322</u>	<u>6894938</u>	150	10/03/2003	SYSTEM AND METHOD OF CALIBRATING A READ CIRCUIT IN A MAGNETIC MEMORY	PERNER, FREDERICK A.
<u>10680464</u>	<u>6990012</u>	150	10/07/2003	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
<u>10680483</u>	<u>7136300</u>	150	10/06/2003	MAGNETIC MEMORY DEVICE INCLUDING GROUPS OF SERIES-CONNECTED MEMORY ELEMENTS	PERNER, FREDERICK A.
<u>10681483</u>	<u>6930370</u>	150	10/08/2003	MEMORY WITH CONDUCTORS BETWEEN OR IN COMMUNICATION WITH STORAGE UNITS	PERNER, FREDERICK A.
<u>10685618</u>	<u>6839271</u>	150	10/15/2003	MAGNETIC MEMORY DEVICE	PERNER, FREDERICK A.
<u>10689144</u>	<u>6847544</u>	150	10/20/2003	MAGNETIC MEMORY WHICH DETECTS CHANGES BETWEEN FIRST AND SECOND RESISTIVE STATES OF MEMORY CELL	PERNER, FREDERICK A.
<u>10690249</u>	<u>6990030</u>	150	10/21/2003	MAGNETIC MEMORY HAVING A CALIBRATION SYSTEM	PERNER, FREDERICK A.
<u>10696826</u>	<u>6873544</u>	150	10/30/2003	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	PERNER, FREDERICK A.
<u>10697776</u>	Not Issued	161	10/30/2003	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	PERNER, FREDERICK A.
<u>10698896</u>	Not Issued	164	10/31/2003	MULTI-SAMPLE READ CIRCUIT HAVING TEST MODE OF OPERATION	PERNER, FREDERICK A.
<u>10713510</u>	<u>6930369</u>	150	11/14/2003	THIN FILM DEVICE AND A METHOD OF PROVIDING THERMAL ASSISTANCE THEREIN	PERNER, FREDERICK A.
<u>10725746</u>	<u>6850430</u>	150	12/02/2003	REGULATING A MAGNETIC MEMORY CELL WRITE CURRENT	PERNER, FREDERICK A.
<u>10725803</u>	<u>6937504</u>	150	12/02/2003	SELECTING A MAGNETIC MEMORY CELL WRITE CURRENT	PERNER, FREDERICK A.

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Day : Friday
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PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = TRAN

First Name = LUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09984934	6504742	150	10/31/2001	3-D-MEMORY DEVICE FOR LARGE STORAGE CAPACITY	TRAN, LUNG T.
09992426	6483734	150	11/26/2001	MEMORY DEVICE HAVING MEMORY CELLS CAPABLE OF FOUR STATES	TRAN, LUNG T.
09998216	6608790	150	12/03/2001	WRITE CURRENT COMPENSATION FOR TEMPERATURE VARIATIONS IN MEMORY ARRAYS	TRAN, LUNG T.
10084111	6678189	150	02/25/2002	METHOD AND SYSTEM FOR PERFORMING EQUIPOTENTIAL SENSING ACROSS A MEMORY ARRAY TO ELIMINATE LEAKAGE CURRENTS	TRAN, LUNG T.
10098204	6593608	150	03/15/2002	MAGNETO-RESISTIVE STORAGE DEVICE HAVING DOUBLE TUNNEL JUNCTION	TRAN, LUNG T.
10098206	6885573	150	03/15/2002	DIODE FOR USE IN MRAM DEVICES AND METHOD OF MANUFACTURE	TRAN, LUNG T.
10098903	6778421	150	03/14/2002	MEMORY DEVICE ARRAY HAVING A PAIR OF MAGNETIC BITS SHARING A COMMON CONDUCTOR LINE	TRAN, LUNG T.
10116497	6967350	150	04/02/2002	MEMORY STRUCTURES	TRAN, LUNG T.
10133605	6673714	150	04/25/2002	METHOD OF FABRICATING A SUB-LITHOGRAPHIC SIZED VIA	TRAN, LUNG T.
10136782	6597598	150	04/30/2002	RESISTIVE CROSS POINT MEMORY ARRAYS HAVING A CHARGE INJECTION	TRAN, LUNG T.

				DIFFERENTIAL SENSE AMPLIFIER	
<u>10136976</u>	<u>6574129</u>	150	04/30/2002	RESISTIVE CROSS POINT MEMEORY CELL ARRAYS HAVING A CROSS-COUPLE LATCH SENSE AMPLIFIER	TRAN, LUNG T.
<u>10151913</u>	Not Issued	161	05/22/2002	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	TRAN, LUNG T.
<u>10151915</u>	<u>6757188</u>	150	05/22/2002	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	TRAN, LUNG T.
<u>10232363</u>	<u>6791865</u>	150	09/03/2002	MEMORY DEVICE CAPABLE OF CALIBRATION AND CALIBRATION METHODS THEREFOR	TRAN, LUNG T.
<u>10234511</u>	<u>6754097</u>	150	09/03/2002	READ OPERATIONS ON MULTI-BIT MEMORY CELLS IN RESISTIVE CROSS POINT ARRAYS	TRAN, LUNG T.
<u>10235011</u>	<u>6577529</u>	150	09/03/2002	MULTI-BIT MAGNETIC MEMORY DEVICE	TRAN, LUNG T.
<u>10235045</u>	<u>6801451</u>	150	09/03/2002	MAGNETIC MEMORY DEVICES HAVING MULTIPLE BITS PER MEMORY CELL	TRAN, LUNG T.
<u>10254559</u>	<u>6577549</u>	150	09/25/2002	WRITE CURRENT COMPENSATION FOR TEMPERATURE VARIATIONS IN MEMORY ARRAYS	TRAN, LUNG T.
<u>10299542</u>	<u>6791867</u>	150	11/18/2002	SELECTION OF MEMORY CELLS IN DATA STORAGE DEVICES	TRAN, LUNG T.
<u>10315748</u>	Not Issued	98	12/10/2002	THERMALLY-ASSISTED SWITCHING OF MAGNETIC MEMORY ELEMENTS	TRAN, LUNG T.
<u>10349685</u>	<u>6693825</u>	150	01/23/2003	MAGNETO-RESISTIVE DEVICE INCLUDING CLAD CONDUCTOR	TRAN, LUNG T.
<u>10351013</u>	<u>6891746</u>	150	01/25/2003	MAGNETO-RESISTIVE DEVICE HAVING SOFT REFERENCE LAYER	TRAN, LUNG T.
<u>10459437</u>	<u>6826079</u>	150	06/10/2003	METHOD AND SYSTEM FOR PERFORMING EQUIPOTENTIAL SENSING ACROSS A MEMORY ARRAY	TRAN, LUNG T.

				TO ELIMINATE LEAKAGE CURRENTS	
<u>10465714</u>	<u>7027319</u>	150	06/19/2003	RETRIEVING DATA STORED IN A MAGNETIC INTEGRATED MEMORY	TRAN, LUNG T.
<u>10657519</u>	<u>6961263</u>	150	09/08/2003	MEMORY DEVICE WITH A THERMALLY ASSISTED WRITE	TRAN, LUNG T.
<u>10661448</u>	Not Issued	71	09/11/2003	Increased magnetic memory array sizes and operating margins	TRAN, LUNG T.
<u>10668405</u>	<u>6865105</u>	150	09/22/2003	THERMAL-ASSISTED SWITCHING ARRAY CONFIGURATION FOR MRAM	TRAN, LUNG T.
<u>10692617</u>	<u>6879508</u>	150	10/24/2003	MEMORY DEVICE ARRAY HAVING A PAIR OF MAGNETIC BITS SHARING A COMMON CONDUCTOR LINE	TRAN, LUNG T.
<u>10695567</u>	<u>7038878</u>	150	10/28/2003	MICROMOVER CONFIGURED TO MOVE A MAGNETIC STORAGE MEDIUM IN PERPENDICULAR DIRECTIONS IN A PLANE	TRAN, LUNG T.
<u>10695710</u>	<u>7057258</u>	150	10/29/2003	RESISTIVE MEMORY DEVICE AND METHOD FOR MAKING THE SAME	TRAN, LUNG T.
<u>10696826</u>	<u>6873544</u>	150	10/30/2003	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	TRAN, LUNG T.
<u>10697776</u>	Not Issued	161	10/30/2003	Equi-potential sensing magnetic random access memory (MRAM) with series diodes	TRAN, LUNG T.
<u>10753298</u>	<u>6791874</u>	150	01/08/2004	MEMORY DEVICE CAPABLE OF CALIBRATION AND CALIBRATION METHODS THEREFOR	TRAN, LUNG T.
<u>10969085</u>	Not Issued	30	10/20/2004	Display device	TRAN, LUNG T.
<u>11089687</u>	Not Issued	161	03/24/2005	Method of manufacture for improved diode for use in MRAM devices	TRAN, LUNG T.
<u>08614981</u>	Not Issued	161	03/12/1996	ESD PROTECTION DEVICE FOR MAGNETIC RECORDING HEAD	TRAN, LUNG T.
<u>08974925</u>	<u>6169686</u>	150	11/20/1997	SOLID-STATE MEMORY WITH	TRAN, LUNG T.

				MAGNETIC STORAGE CELLS	
09334485	6111783	150	06/16/1999	MRAM DEVICE INCLUDING WRITE CIRCUIT FOR SUPPLYING WORD AND BIT LINE CURRENT HAVING UNEQUAL MAGNITUDES	TRAN, LUNG T.
09370087	6163477	150	08/06/1999	MRAM DEVICE USING MAGNETIC FIELD BIAS TO IMPROVE REPRODUCIBILITY OF MEMORY CELL SWITCHING	TRAN, LUNG T.
09430238	6262625	150	10/29/1999	OPERATIONAL AMPLIFIER WITH DIGITAL OFFSET CALIBRATION	TRAN, LUNG T.
09430611	6188615	150	10/29/1999	MRAM DEVICE INCLUDING DIGITAL SENSE AMPLIFIERS	TRAN, LUNG T.
10093344	6625059	150	03/06/2002	SYNTHETIC FERRIMAGNET REFERENCE LAYER FOR A MAGNETIC STORAGE DEVICE	TRAN, LUNG THE
10176179	6507513	150	06/20/2002	USING DELAYED ELECTRICAL PULSES WITH MAGNETO-RESISTIVE DEVICES	TRAN, LUNG THE
10680483	7136300	150	10/06/2003	MAGNETIC MEMORY DEVICE INCLUDING GROUPS OF SERIES-CONNECTED MEMORY ELEMENTS	TRAN, LUNG THE
07209651	4878140	150	06/21/1988	MAGNETO-RESISTIVE SENSOR WITH OPPOSING CURRENTS FOR READING PERPENDICULARLY RECORDED MEDIA	TRAN, LUNG THE
10196877	6850433	150	07/15/2002	MAGNETIC MEMORY DEVICE AND METHOD	TRAN, LUNG THE

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Inventor Name Search Result

Your Search was:

Last Name = ELDREDGE

First Name = KENNETH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10743662	7266732	150	12/22/2003	MRAM WITH CONTROLLER	ELDREDGE, KENNETH
09498587	6185143	150	02/04/2000	MAGNETIC RANDOM ACCESS MEMORY (MRAM) DEVICE INCLUDING DIFFERENTIAL SENSE AMPLIFIERS	ELDREDGE, KENNETH I.
09483383	6728799	150	01/13/2000	HYBRID DATA I/O FOR MEMORY APPLICATIONS	ELDREDGE, KENNETH J
09498588	6584589	150	02/04/2000	SELF-TESTING OF MAGNETO-RESISTIVE MEMORY ARRAYS	ELDREDGE, KENNETH J.
09564713	6256224	150	05/03/2000	Write circuit for large mram arrays	ELDREDGE, KENNETH J.
09598671	6317376	150	06/20/2000	Reference signal generation for magnetic random access memory devices	ELDREDGE, KENNETH J.
09759952	6782616	150	01/12/2001	CONNECTION ARRANGEMENTS FOR ELECTRICAL DEVICES	ELDREDGE, KENNETH J.
09800561	6590850	150	03/07/2001	PACKAGING FOR STORAGE DEVICES USING ELECTRON EMISSIONS	ELDREDGE, KENNETH J.
09809707	6385111	150	03/14/2001	REFERENCE SIGNAL GENERATION FOR MAGNETIC RANDOM ACCESS MEMORY DEVICES	ELDREDGE, KENNETH J.
09811299	6791931	150	03/16/2001	ACCELEROMETER USING FIELD EMITTER TECHNOLOGY	ELDREDGE, KENNETH J.
09827114	6363000	150	04/05/2001	Write circuit for large MRAM arrays	ELDREDGE, KENNETH J.
09915179	Not	161	07/25/2001	Fault tolerant magnetoresistive	ELDREDGE,

	Issued			solid-state storage device	KENNETH J.
<u>09922436</u>	<u>6671778</u>	150	08/03/2001	REDUNDANT ARRAY OF INDEPENDENT STORAGE DEVICES	ELDREDGE, KENNETH J.
<u>10017567</u>	<u>6534841</u>	150	12/14/2001	CONTINUOUS ANTIFUSE MATERIAL IN MEMORY STRUCTURE	ELDREDGE, KENNETH J.
<u>10044542</u>	<u>6643195</u>	150	01/11/2002	SELF-HEALING MRAM	ELDREDGE, KENNETH J.
<u>10051476</u>	<u>6775223</u>	150	01/17/2002	SYSTEMS FOR CONTROLLING STORAGE DEVICE EMITTERS	ELDREDGE, KENNETH J.
<u>10120113</u>	<u>6625055</u>	150	04/09/2002	MULTIPLE LOGICAL BITS PER MEMORY CELL IN A MEMORY DEVICE	ELDREDGE, KENNETH J.
<u>10120118</u>	Not Issued	161	04/09/2002	Non-volatile, multi-level memory device	ELDREDGE, KENNETH J.
<u>10151915</u>	<u>6757188</u>	150	05/22/2002	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM) WITH SERIES DIODES	ELDREDGE, KENNETH J.
<u>10295651</u>	Not Issued	71	11/15/2002	Transferring data in selectable transfer modes	ELDREDGE, KENNETH J.
<u>10314114</u>	<u>6836429</u>	150	12/07/2002	MRAM HAVING TWO WRITE CONDUCTORS	ELDREDGE, KENNETH J.
<u>10384053</u>	Not Issued	164	03/06/2003	ASSISTED MEMORY SYSTEM	ELDREDGE, KENNETH J.
<u>10609904</u>	<u>6906964</u>	150	06/27/2003	MULTIPLE BUFFER MEMORY INTERFACE	ELDREDGE, KENNETH J.
<u>10611544</u>	<u>7019381</u>	150	07/01/2003	METHOD OF PROVIDING MULTIPLE LOGICAL BITS PER MEMORY CELL	ELDREDGE, KENNETH J.
<u>10661448</u>	Not Issued	71	09/11/2003	Increased magnetic memory array sizes and operating margins	ELDREDGE, KENNETH J.
<u>10676465</u>	<u>6927996</u>	150	09/30/2003	MAGNETIC MEMORY DEVICE	ELDREDGE, KENNETH J.
<u>10695363</u>	<u>7213086</u>	150	10/28/2003	SYSTEM HAVING A STORAGE CONTROLLER THAT MODIFIES OPERATION OF A STORAGE SYSTEM BASED ON THE STATUS OF A DATA TRANSFER	ELDREDGE, KENNETH J.
<u>10696826</u>	<u>6873544</u>	150	10/30/2003	TRIPLE SAMPLE SENSING FOR MAGNETIC RANDOM ACCESS MEMORY (MRAM)	ELDREDGE, KENNETH J.

				WITH SERIES DIODES	
<u>10727402</u>	<u>6982571</u>	150	12/04/2003	SYSTEMS AND METHODS FOR TRANSLATING VOLTAGE LEVELS OF DIGITAL SIGNALS	ELDREDGE, KENNETH J.
<u>10792264</u>	<u>6916182</u>	150	03/03/2004	Connection arrangements for electrical devices having a ledge on which contact terminals are provided	ELDREDGE, KENNETH J.
<u>10983776</u>	<u>7102918</u>	150	11/07/2004	MRAM HAVING TWO WRITE CONDUCTORS	ELDREDGE, KENNETH J.
<u>11264539</u>	Not Issued	41	11/01/2005	Controllably connectable strings of MRAM cells	ELDREDGE, KENNETH J.
<u>11834565</u>	Not Issued	19	08/06/2007	SOLID STATE STORAGE ELEMENT AND METHOD	ELDREDGE, KENNETH J.
<u>60835624</u>	Not Issued	159	08/05/2006	Flash hard drive and method	ELDREDGE, KENNETH J.
<u>60886926</u>	Not Issued	20	01/27/2007	Solid State Storage Element and Method	ELDREDGE, KENNETH J.
<u>08589674</u>	Not Issued	161	01/22/1996	APPARATUS AND METHOD FOR THERMAL ASPERITY DETECTION AND FAILURE PREDICTION IN DISK DRIVE SYSTEMS	ELDREDGE, KENNETH J.
<u>09430238</u>	<u>6262625</u>	150	10/29/1999	OPERATIONAL AMPLIFIER WITH DIGITAL OFFSET CALIBRATION	ELDREDGE, KENNETH J.
<u>09430611</u>	<u>6188615</u>	150	10/29/1999	MRAM DEVICE INCLUDING DIGITAL SENSE AMPLIFIERS	ELDREDGE, KENNETH J.
<u>10448574</u>	<u>6873543</u>	150	05/30/2003	MEMORY DEVICE	ELDREDGE, KENNETH JAMES
<u>10454463</u>	<u>6839275</u>	150	06/04/2003	MEMORY SYSTEM	ELDREDGE, KENNETH JAMES
<u>10695567</u>	<u>7038878</u>	150	10/28/2003	MICROMOVER CONFIGURED TO MOVE A MAGNETIC STORAGE MEDIUM IN PERPENDICULAR DIRECTIONS IN A PLANE	ELDREDGE, KENNETH JAMES
<u>10718137</u>	<u>6967149</u>	150	11/20/2003	STORAGE STRUCTURE WITH CLEAVED LAYER	ELDREDGE, KENNETH JAMES

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